

What is claimed is:

1. A digital recording and reproducing apparatus which is capable of having a removable external memory
5 mounted therein, for performing recording of recording data in the external memory and reproduction based on the recording data recorded in the external memory,
the digital recording and reproducing apparatus comprising:
10 an identification data-generating block for generating identification data for identifying the external memory individually;
an internal memory in which the identification data can be recorded; and
15 a control block for recording the identification data in the external memory and said internal memory, and carrying out identification data determination processing for determining, when mounting of the external memory is detected, whether or not the
20 identification data recorded in the external memory and the identification data recorded in said internal memory are identical to each other.
2. A digital recording and reproducing apparatus according to claim 1, wherein said control block
25 records the identification data in both of the external memory and said internal memory before the recording data is recorded in the external memory.
3. A digital recording and reproducing apparatus according to claim 2, wherein said control block
30 carries out the identification data determination processing when the external memory is removed and mounted during recording of the recording data in the external memory, said control block continuing to

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record the recording data in the mounted external memory when the identification data recorded in the external memory and the identification data recorded in said internal memory are identical to each other, and

5 carrying out predetermined error-handling processing when the identification data recorded in the external memory and the identification data recorded in said internal memory are different from each other.

4. A digital recording and reproducing apparatus

10 according to claim 2, wherein said control block causes the recording data to be recorded in said internal memory when the external memory is removed during recording of the recording data in the external memory, carries out the identification data determination

15 processing when the external memory is mounted, and records the recording data recorded in said internal memory, in the mounted external memory when the identification data recorded in the external memory and the identification data recorded in said internal

20 memory are identical to each other.

5. A digital recording and reproducing apparatus according to any one of claims 1 to 4, wherein said identification data-generating block generates numerical value information at random to thereby

25 generate the identification data.

6. A digital recording and reproducing apparatus according to claim any one of claims 1 to 4, wherein the recording data is sound data, and wherein the digital recording and reproducing apparatus includes a

30 sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound

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signal generation block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for
5 amplifying the converted analog sound signal to output the amplified sound signal.

7. A digital recording and reproducing apparatus according to claim 5, wherein the recording data is sound data, and wherein the digital recording and
10 reproducing apparatus includes a sound signal input block for inputting an analog sound signal, a sound data generation block for generating the sound data by converting the analog sound signal to digital data and compressing the digital data, a sound signal generation
15 block for decompressing the sound data recorded in the external memory to generate the digital data and converting the digital data to the analog sound signal, and an amplifier circuit for amplifying the converted analog sound signal to output the amplified sound
20 signal.

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